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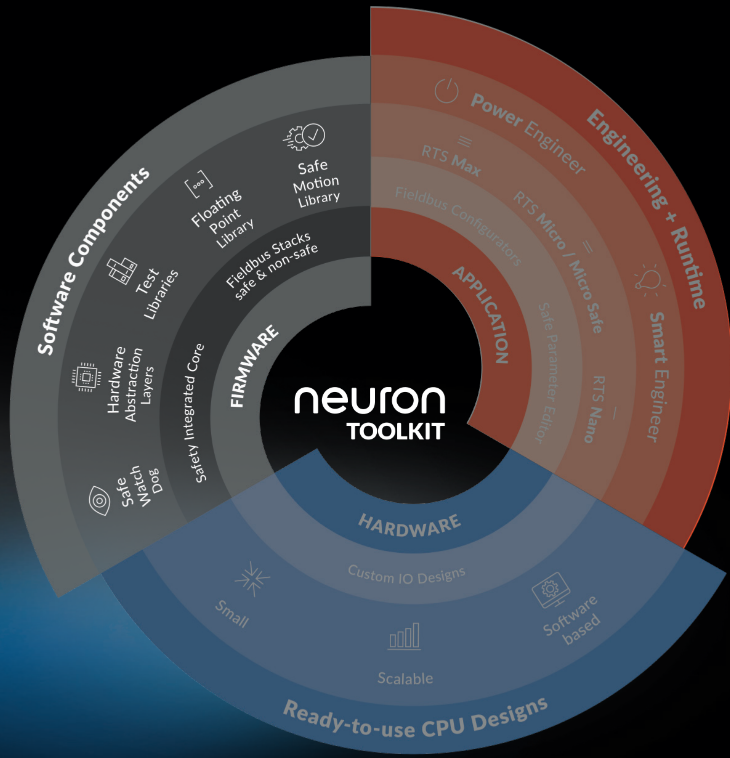
FSoE-Host/ Device Stack



Summary

The highly compact and efficient implementation of the FSoE host/device stack V2 enables integration into very small and cost-effective hardware structures and is characterised by its high-performance runtime behaviour. Connection to existing EtherCAT structures is possible with little effort thanks to the clear interface structure of the stack. If the original sources are used and the supplied requirements are adhered to or implemented, no further work is required for a type examination by a notified body. Single-channel integration for medium-high safety requirements is also possible (up to SIL2 in accordance with IEC 61508).

Main benefits at one glance

The implementation is supplied as an easy-to-integrate library without any further requirements on the hardware platform.

- Runs with and without operating system
- Shorter time-to-market thanks to pre-certification
- Simple connection to a standard (non-safe) EtherCAT communication module
- Pre-certification to achieve PLe/SIL3
- Functional Safety

Operating system independent - Operates with and without operating system

Simple connection to a standard (non-safe) EtherCAT communication module thanks to abstraction layers possible

Integration using the Integration Guide

Shorter time-to-market thanks to pre-certification (up to SIL3 according to IEC 61508 or PLe according to ISO 13849)

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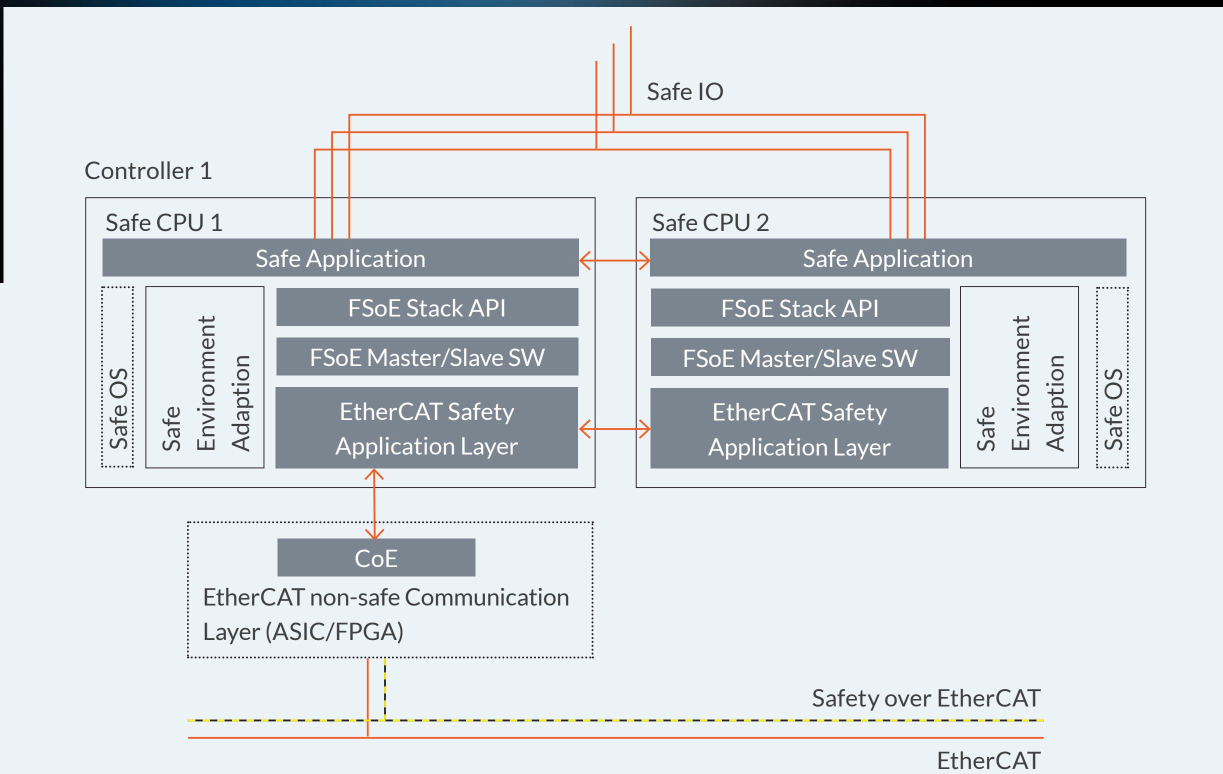
Your Solution Partner for Functional Safety, Engineering Tools & Runtimes

Architecture



Specifications

Multiple instantiation enables parallel integration of host and device stack on one device

Can be used on 3 different hardware architectures:

- Dual-channel on two processors
- Dual-channel on one processor (virtual dual-channel)
- Single-channel on one processor (max SIL2)

In addition to EtherCAT, the following other Industrial Ethernet protocols can be realised:

- Ethernet
- POWERLINK
- EtherNet/IP
- PROFINET

Minimal footprint (approx. 18kB)

Product was tested using EtherCAT conformance test according to ETG

Available as source code (C) or compilable binary

Customised generation of the binary file according to the extensive configuration sheet

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