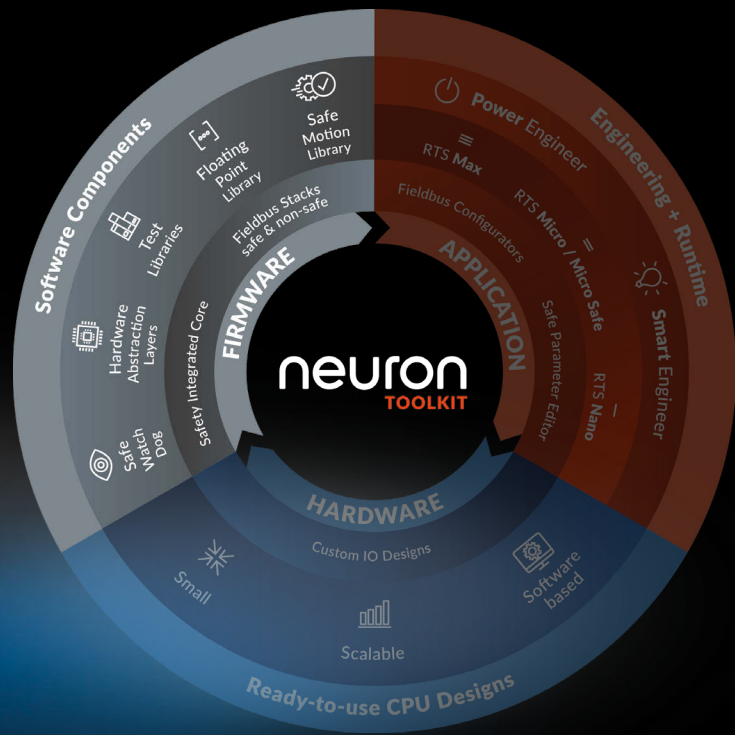


Safety Integrated Core



Summary

The SIC firmware core provides base functionality for any SIL3 or PLe rated safety device. It forms the basis of the firmware in our safety toolkit. It can be combined with any of our SIC hardware architectures. It can also be combined with any existing or custom hardware design.

Main benefits at one glance



Simple and easy integration into custom hardware



Process data management



Cycle time, runtime and program flow monitoring



Shorter time-to-market



Precertification to achieve high safety levels (PLe/SIL3)

Hardware independent with hardware abstraction layer (HAL) that connects with customer specific hardware design. Also available for Neuron SIC hardware series

Simple and easy to integration with any hardware design

Process data management between two runtime channels and IO/fieldbus

Flexible assignment of physical I/O to logical channels allows creation of new hardware variants without recertification

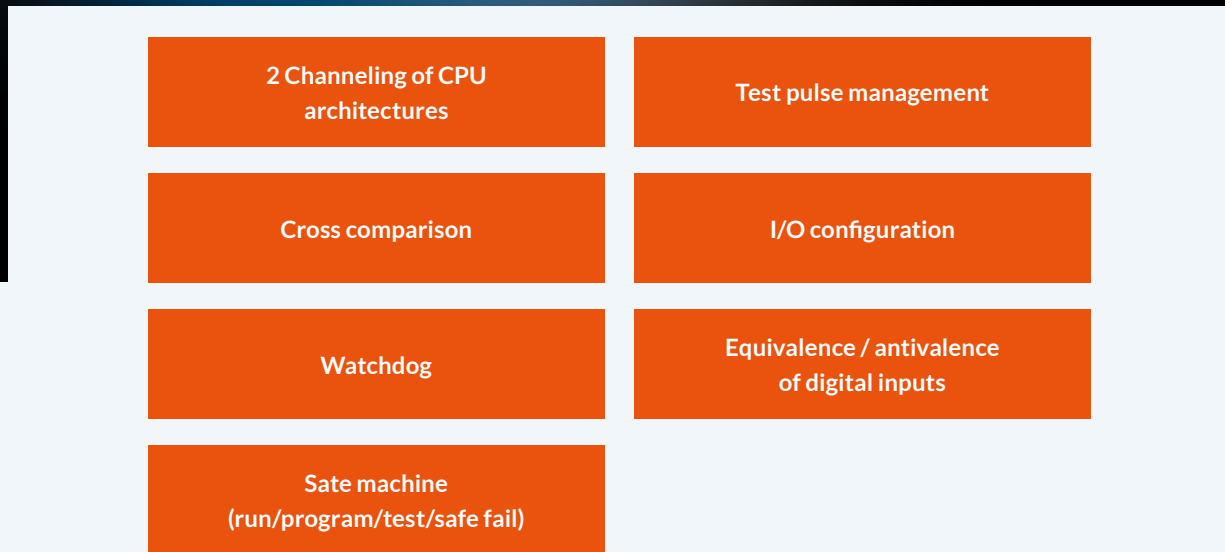
Configuration and execution of I/O signal tests (OSSD, test pulses, equivalent/antivalent input monitoring)

Cycle time, runtime and program flow monitoring

Shorter time-to-market due to precertification to achieve high safety levels up to up to PLe according to ISO 13849 or SIL according to IEC 61508-1 standard

Your Solution Partner for Functional Safety, Engineering Tools & Runtimes

Functional overview



Specifications

Modular software with an object-oriented concept

1 or 2 channel architectures possible